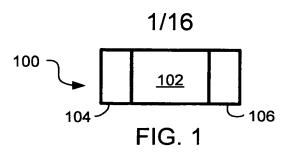
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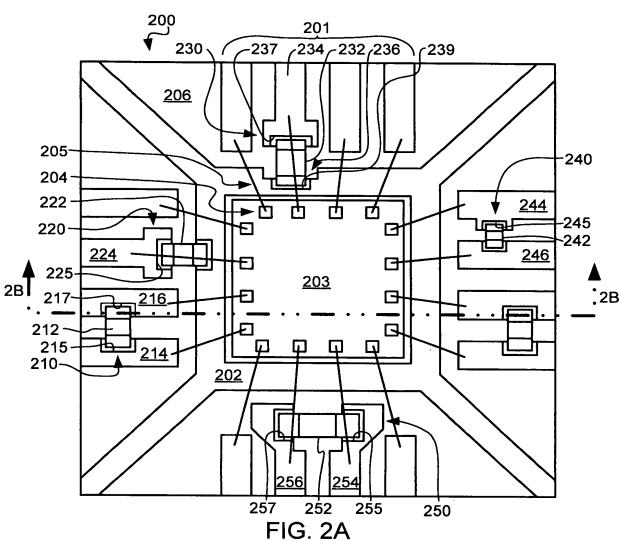
Title: SEMICONDUCTOR PACKAGE WITH PASSIVE DEVICE INTEGRATION

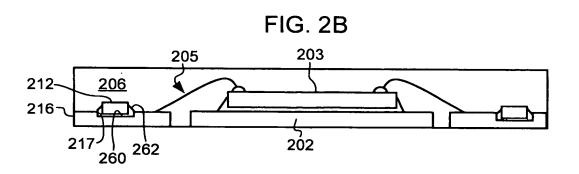
Inventors: Seng Guan Chow, et al.

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Contact: Mikio Ishimaru (408) 738-0592





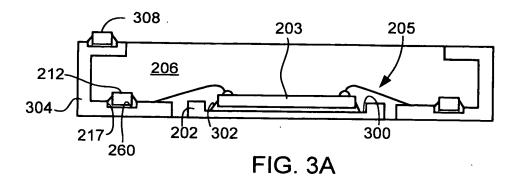


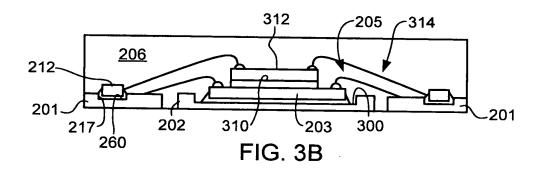
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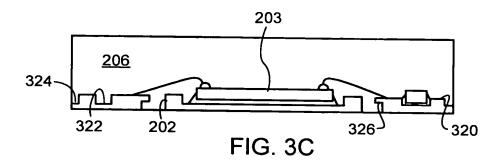
Title: SEMICONDUCTOR PACKAGE WITH PASSIVE DEVICE INTEGRATION Inventors: Seng Guan Chow, et al.

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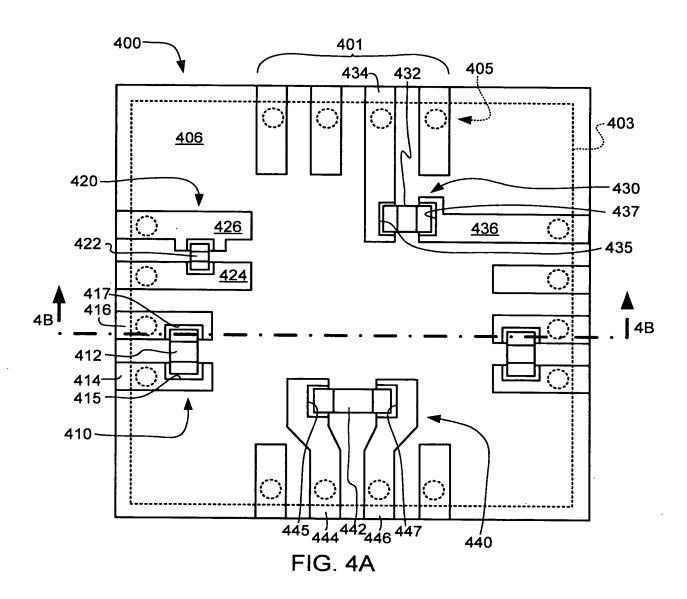


FIG. 4B 400~ 412 430 416 405 <u>406</u> 456 450 -452 454

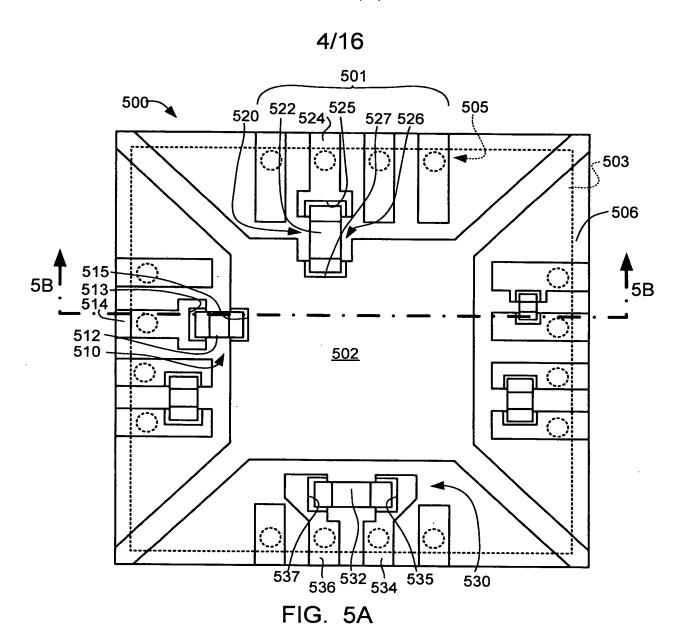
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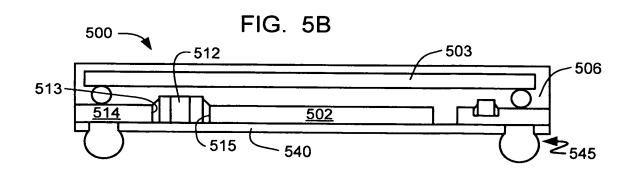
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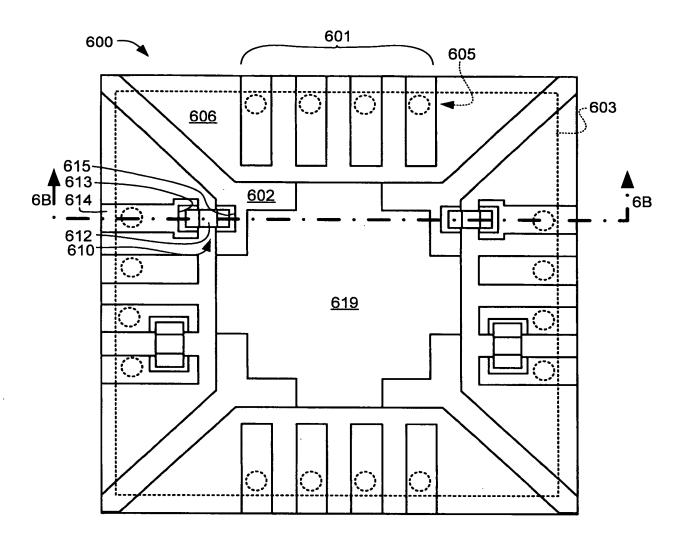
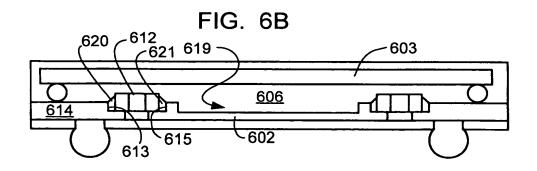


FIG. 6A



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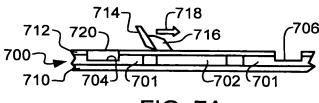
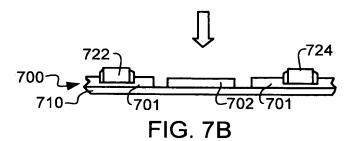
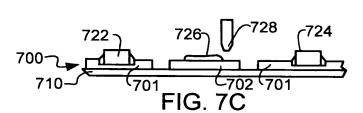
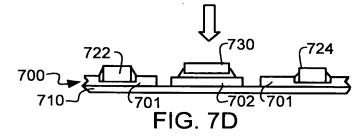
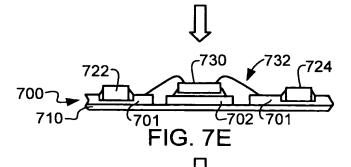


FIG. 7A







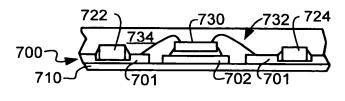


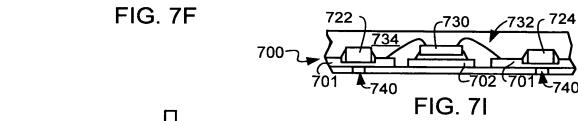
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Title: SEMICONDUCTOR PACKAGE WITH PASSIVE DEVICE INTEGRATION

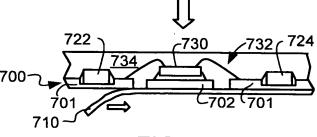
Inventors: Seng Guan Chow, et al.

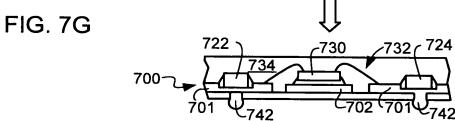
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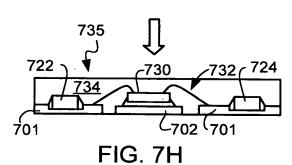
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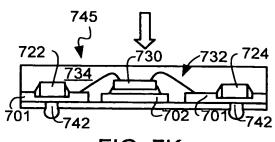


FIG. 7J

FIG. 7K

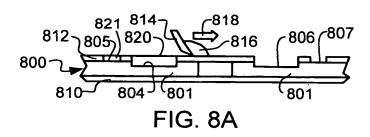
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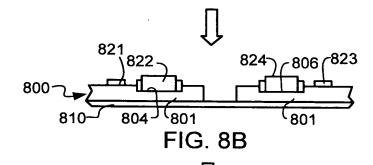
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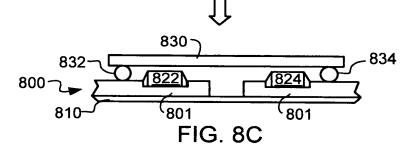
Inventors: Seng Guan Chow, et al.

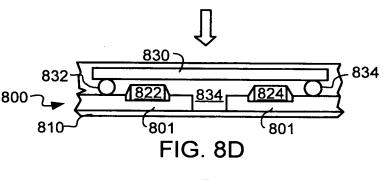
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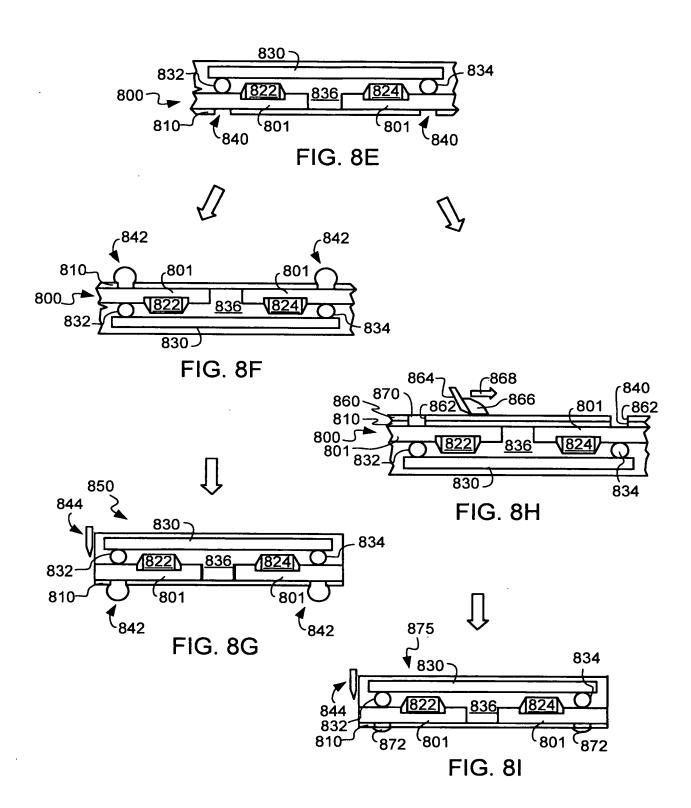
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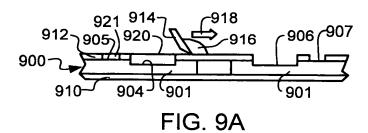
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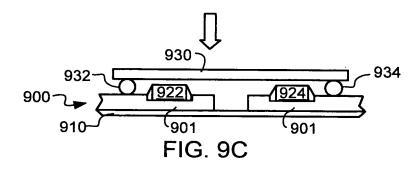
Inventors: Seng Guan Chow, et al.

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921 922 924 906 923 900 904 901 901 FIG. 9B



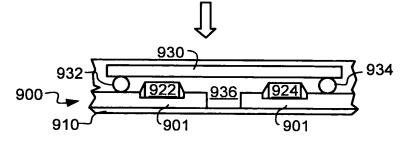


FIG. 9D



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<u>936</u>

FIG. 9H

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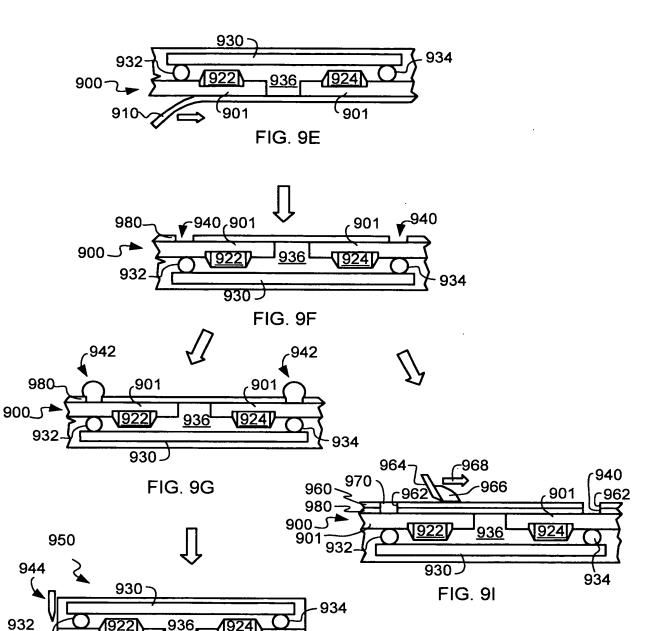
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₹ 930

<sup>(</sup>901

FIG. 9J

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901 972

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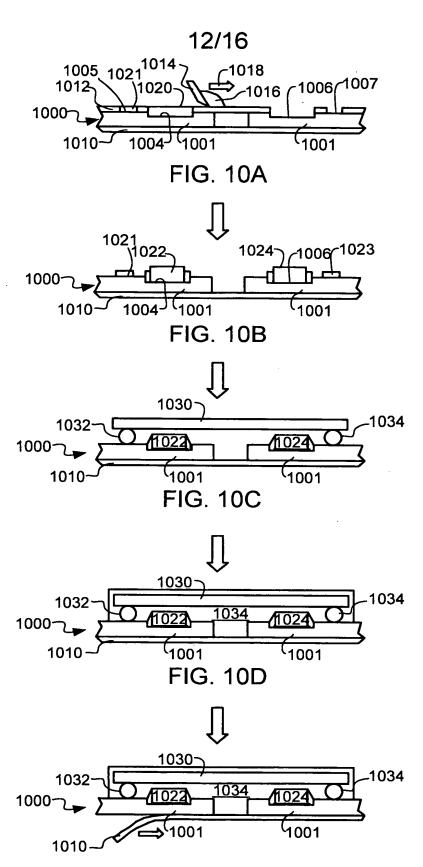


FIG. 10E

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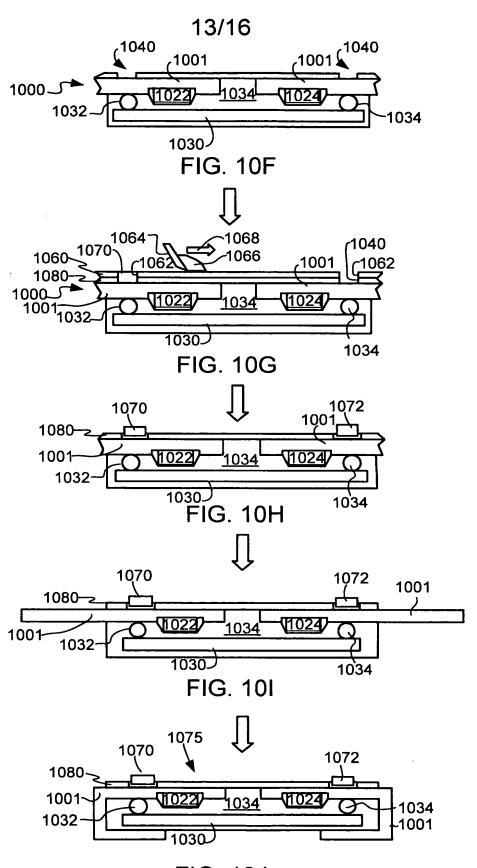


FIG. 10J

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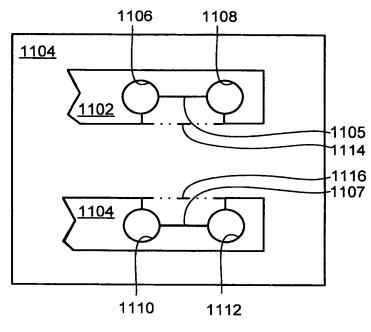


FIG. 11A

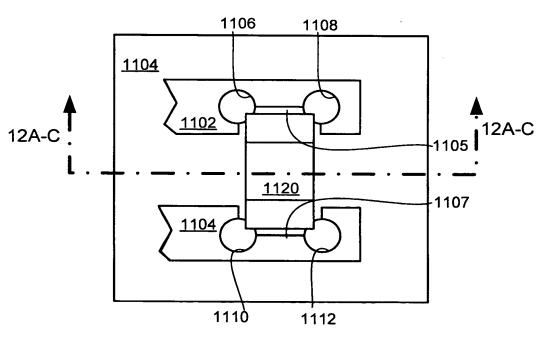


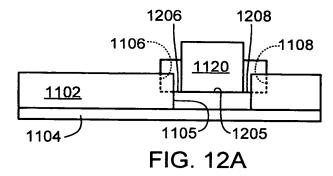
FIG. 11B

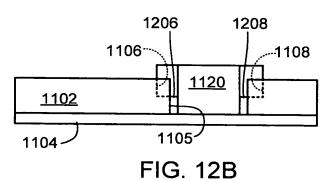
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Title: SEMICONDUCTOR PACKAGE WITH PASSIVE DEVICE INTEGRATION

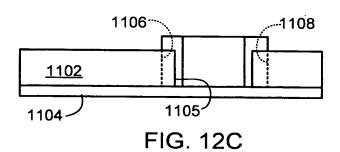
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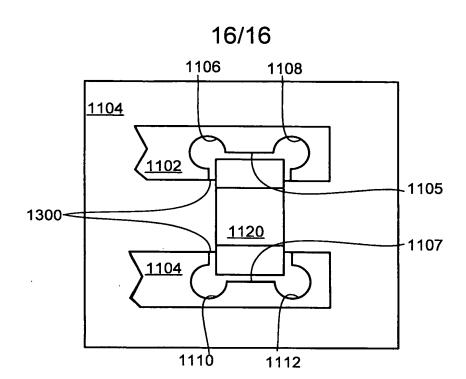


FIG. 13

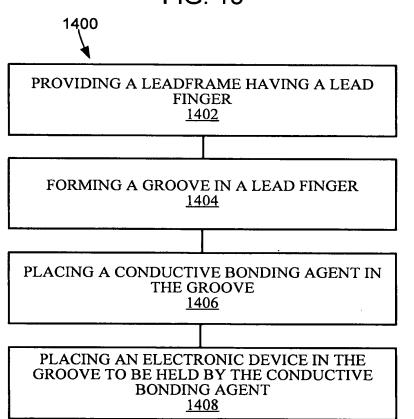


FIG. 14